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L1	7442	partition\$3 same rout\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/09 17:43
L2	358	(partition\$3 same rout\$4) and diagonal and stor\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/09 17:43
L3	358	((partition\$3 same rout\$4) and diagonal and stor\$4) and rout\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/09 17:43
L4	233	((((partition\$3 same rout\$4) and diagonal and stor\$4) and rout\$4) and region	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/09 17:43
L5	195	(((((partition\$3 same rout\$4) and diagonal and stor\$4) and rout\$4) and region) and (edge or shape)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/09 17:43
L6	172	((((((partition\$3 same rout\$4) and diagonal and stor\$4) and rout\$4) and region) and (edge or shape)) and path	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/09 17:43
L7	51	((((((((partition\$3 same rout\$4) and diagonal and stor\$4) and rout\$4) and region) and (edge or shape)) and path) and "716"/\$. ccls.) and grid	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/09 17:43
L8	84	(partition\$3 same rout\$4) same diagonal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/09 17:43
L9	62	((partition\$3 same rout\$4) same diagonal) and rout\$4 and (region or sub-region)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/09 17:43

L10	60	(((partition\$3 same rout\$4) same diagonal) and rout\$4 and (region or sub-region)) and (edge or shape)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/09 17:43
L11	80	((((((partition\$3 same rout\$4) and diagonal and stor\$4) and rout\$4) and region) and (edge or shape)) and path) and "716"/\$.ccis.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/09 17:43

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☐ Check to search within this result set**Results Key:****JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**1 An algorithm for L-shaped channel routing in a diagonal model**

Song, X.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on, Volume: 11, Issue: 2, Feb. 1992
Pages:267 - 270
[\[Abstract\]](#) [\[PDF Full-Text \(356 KB\)\]](#) IEEE JNL
2 Channel routing in Manhattan-diagonal model

Das, S.; Bhattacharya, B.;

VLSI Design, 1996. Proceedings., Ninth International Conference on, 3-6 Jan. 1996
Pages:43 - 48
[\[Abstract\]](#) [\[PDF Full-Text \(568 KB\)\]](#) IEEE CNF
3 Routing of L-shaped channels, switchboxes and staircases in Manhattan-diagonal model

Das, S.; Sur-Kolay, S.; Bhattacharya, B.B.;

VLSI Design, 1998. Proceedings., 1998 Eleventh International Conference on Jan. 1998
Pages:65 - 70
[\[Abstract\]](#) [\[PDF Full-Text \(592 KB\)\]](#) IEEE CNF

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